

PATENT NUMBER

<p>ff</p> <p>O.I.P.E.</p> <p>SCANNED <u>AT (1)</u> Q.A. <u>AA</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/591523	CONT/PRIOR D F	CLASS <del>430</del> 174	SUBCLASS 2.0	ART UNIT <del>1756</del> 2841	EXAMINER NORRIS <del>CUNEO</del>
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TS Fujio Kuwako

TITLE APPLICANTS

~~Method for producing multi-layer printed w. lsg boards having blind~~  
vias

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				<b>ISSUE FEE</b>	
	Amount Due		Date Paid		
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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